

ABSTRACT OF THE DISCLOSURE

An ESD protection circuit comprising a first pad, a second pad, and a clamp circuit. The first pad is used as an external connection terminal to be connected 5 to a semiconductor integrated circuit. The second pad is used as an external connection terminal to be connected to the semiconductor integrated circuit. The clamp circuit is connected between the first pad and the second pad. The ESD protecting circuit further 10 comprises a control circuit. The control circuit controls the clamp circuit, rendering the same conducting or non-conducting.